

IP Blocks

Here are some descriptions about IP blocks used in Loongson processors.

System

- [Local Interrupt Controller](#)
- PLL

Connectivity

- Ethernet MAC

Multimedia

- HDA
- AC97
- I2S
- Display Controller
- Vivante GC1000

From:

<https://wiki.chuang.ac.cn/> - 创学院百科

Permanent link:

https://wiki.chuang.ac.cn/loongson:ip_block

Last update: **2022/09/23 20:14**

